JMSL1040AUQ

100V 29mΩ N-Ch Power MOSFET

Features

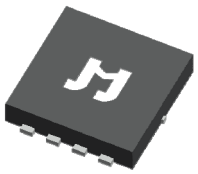
- Low ON-resistance, $R_{DS(ON)}$
- Low Gate Charge, Q_g
- 100% UIS and R_g Tested
- Pb-free Lead Plating
- Halogen-free and RoHS-compliant
- AEC-Q101 Qualified for Automotive Applications

Product Summary

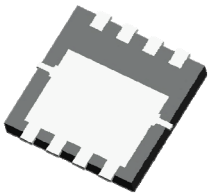
Parameter	Value	Unit
V_{DS}	100	V
$V_{GS(th)\_Typ}$	1.8	V
I_D (@ V_{GS} = 10V) (1)	20	A
$R_{DS(ON)\_Typ}$ (@ V_{GS} = 10V)	29	mΩ
$R_{DS(ON)\_Typ}$ (@ V_{GS} = 4.5V)	39	mΩ

PDFN3x3-8L

Top View

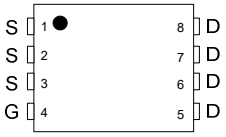


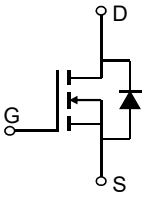
Bottom View



Pin Configuration

Top View



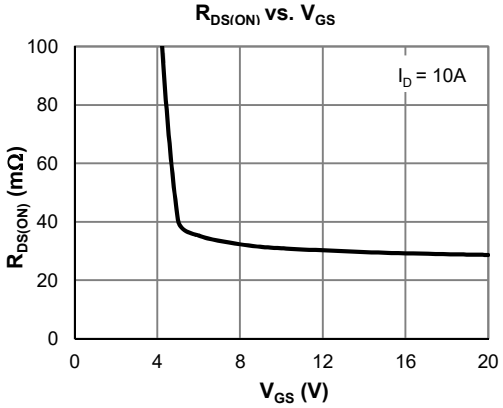


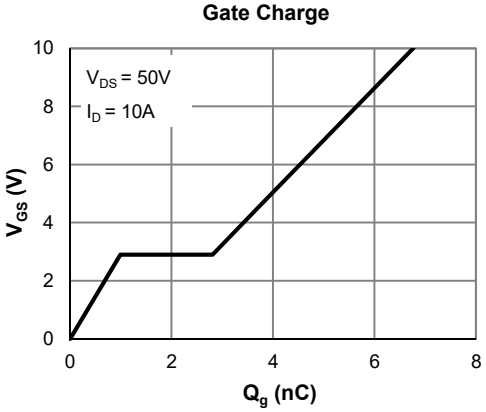
Ordering Information

Device	Package	# of Pins	Marking	MSL	T_J (°C)	Media	Quantity (pcs)
JMSL1040AUQ-13	PDFN3x3-8L	8	SL1040AQ	1	-55 to 150	13-inch Reel	5000

Absolute Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain-to-Source Voltage		V_{DS}	100	V
Gate-to-Source Voltage		V_{GS}	±20	V
Continuous Drain Current (1)	T_C = 25°C	I_D	20	A
	T_C = 100°C		12	
Pulsed Drain Current (2)		I_{DM}	79	A
Avalanche Current (3)		I_{AS}	17	A
Avalanche Energy (3)		E_{AS}	14	mJ
Power Dissipation (4)	T_C = 25°C	P_D	26	W
	T_C = 100°C		10.4	
Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C





Electrical Characteristics (@ T_J = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D = 250μA, V_{GS} = 0V	100			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} =80V, V_{GS} = 0V			1.0	μA
		T_J = 55°C			5.0	
Gate-Body Leakage Current	I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.2	1.8	2.5	V
Static Drain-Source ON-Resistance	$R_{DS(ON)}$	V_{GS} = 10V, I_D = 10A		29	36	mΩ
		V_{GS} = 4.5V, I_D = 6A		39	50	mΩ
Forward Transconductance	g_{FS}	V_{DS} = 5V, I_D = 10A		28		S
Diode Forward Voltage	V_{SD}	I_S = 1A, V_{GS} = 0V		0.70	1.0	V
Diode Continuous Current	I_S	T_C = 25°C			26	A
DYNAMIC PARAMETERS [5]						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 50V, f = 1MHz		363		pF
Output Capacitance	C_{oss}			85		pF
Reverse Transfer Capacitance	C_{rss}			3.0		pF
Gate Resistance	R_g	V_{GS} = 0V, V_{DS}= 0V, f = 1MHz		2.6		Ω
SWITCHING PARAMETERS [5]						
Total Gate Charge (@ V_{GS} = 10V)	Q_g	V_{GS} = 0 to 10V V_{DS} = 50V, I_D = 10A		6.8		nC
Total Gate Charge (@ V_{GS} = 4.5V)	Q_g			3.7		nC
Gate Source Charge	Q_{gs}			1.0		nC
Gate Drain Charge	Q_{gd}			1.8		nC
Turn-On DelayTime	$t_{D(on)}$	V_{GS} = 10V, V_{DS} = 50V R_L = 2.5Ω, R_{GEN} = 6Ω		4.9		ns
Turn-On Rise Time	t_r			16.6		ns
Turn-Off DelayTime	$t_{D(off)}$			11.2		ns
Turn-Off Fall Time	t_f			4.9		ns
Body Diode Reverse Recovery Time	t_{rr}	I_F = 10A, dI_F/dt = 100A/μs		33		ns
Body Diode Reverse Recovery Charge	Q_{rr}	I_F = 10A, dI_F/dt = 100A/μs		45		nC

Thermal Performance

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	68	80	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	4.8	5.8	°C/W

Notes:

1. Computed continuous current assumes the condition of T_{J_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under T_{J_Max} = 150°C.
3. This single-pulse measurement was taken under the following condition [L = 100μH, V_{GS} = 10V, V_{DS} = 50V] while its value is limited by T_{J_Max} = 150°C.
4. The power dissipation P_D is based on T_{J_Max} = 150°C.
5. This value is guaranteed by design hence it is not included in the production test.

Typical Electrical & Thermal Characteristics

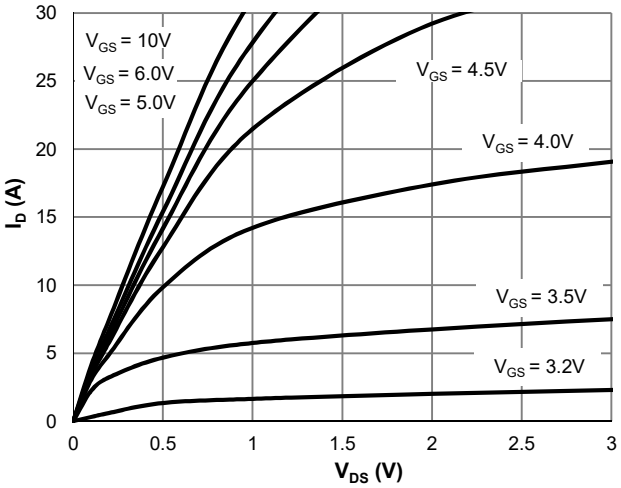


Figure 1: Saturation Characteristics

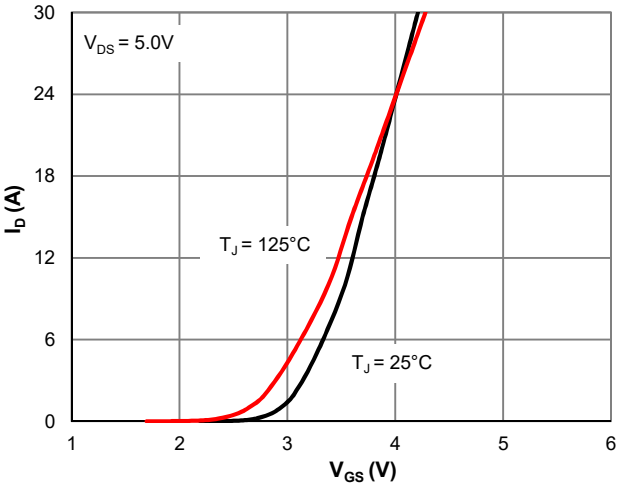


Figure 2: Transfer Characteristics

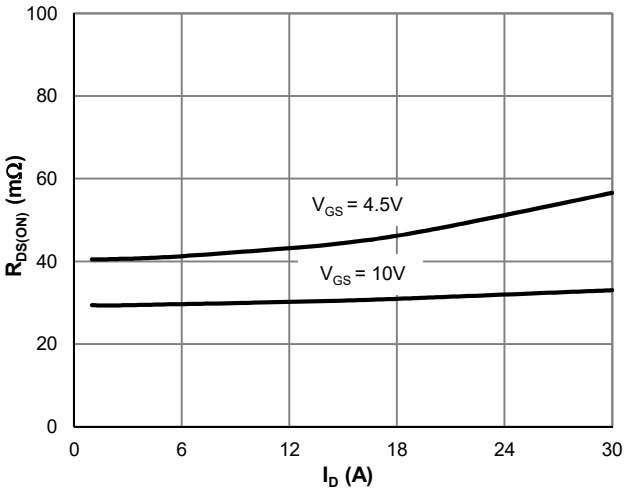


Figure 3: $R_{DS(ON)}$ vs. Drain Current

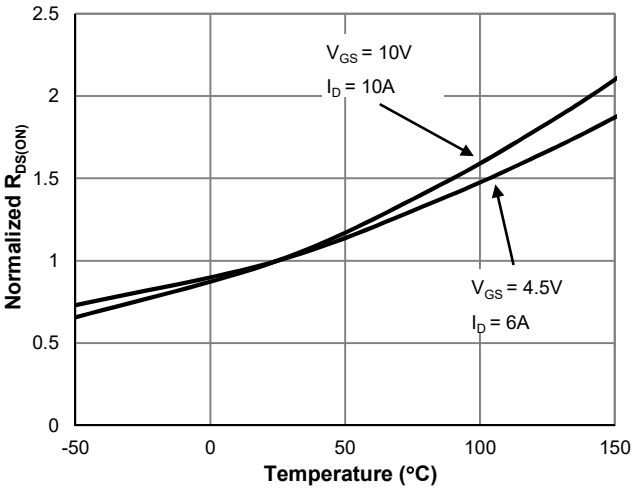


Figure 4: $R_{DS(ON)}$ vs. Junction Temperature

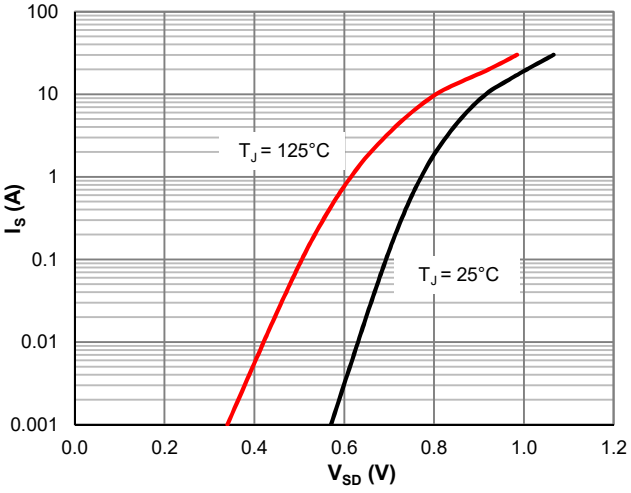


Figure 5: Body-Diode Characteristics

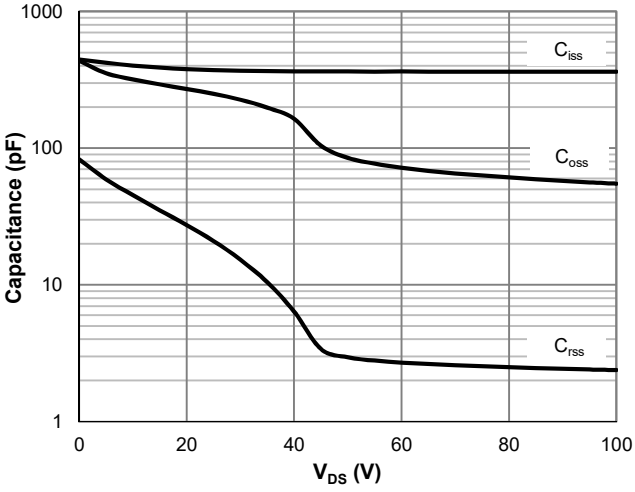


Figure 6: Capacitance Characteristics

Typical Electrical & Thermal Characteristics

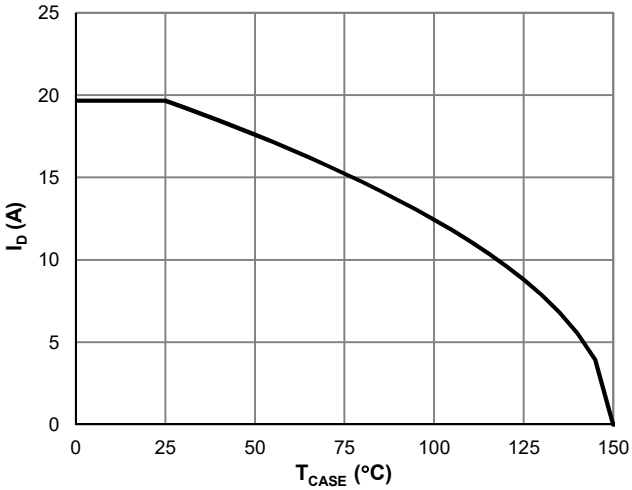


Figure 7: Current De-rating

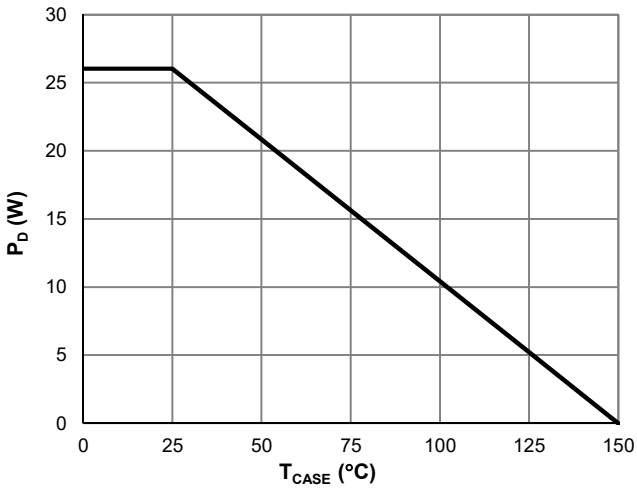


Figure 8: Power De-rating

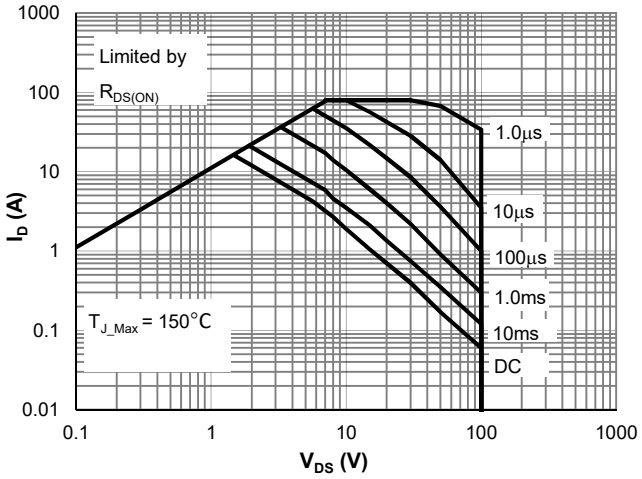


Figure 9: Maximum Safe Operating Area

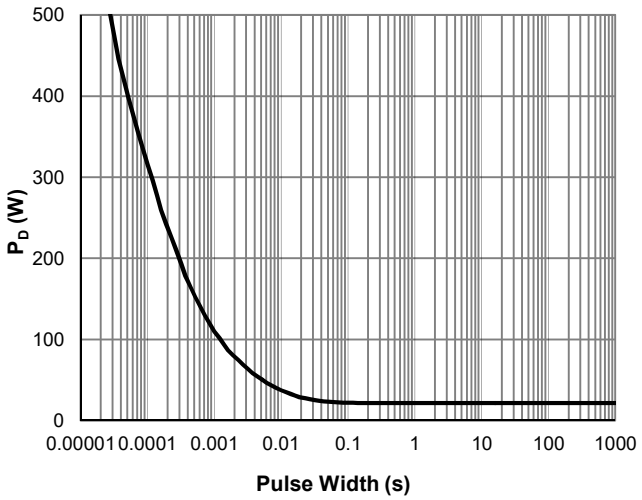


Figure 10: Single Pulse Power Rating, Junction-to-Case

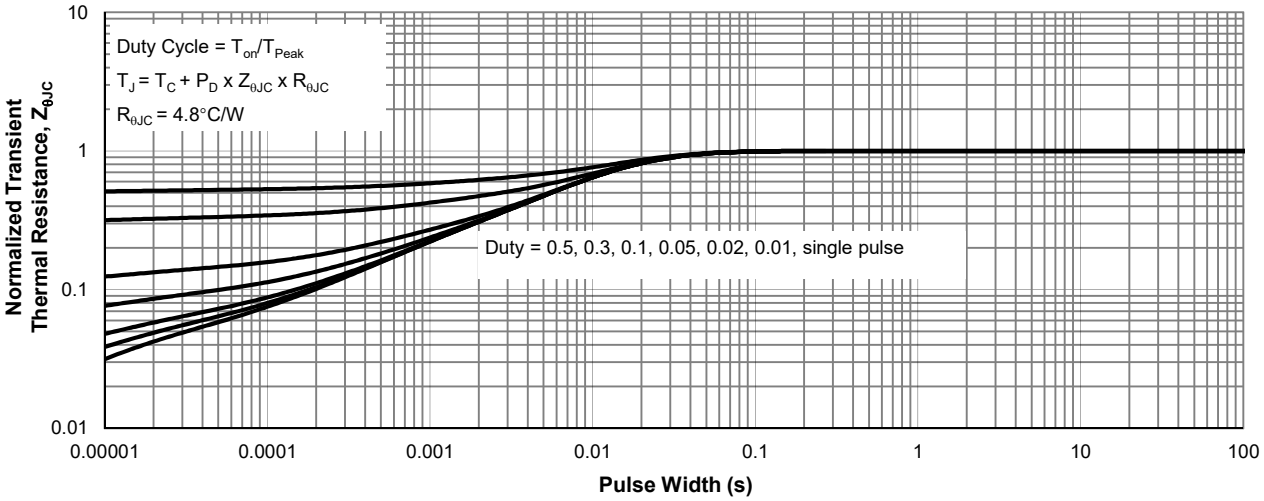
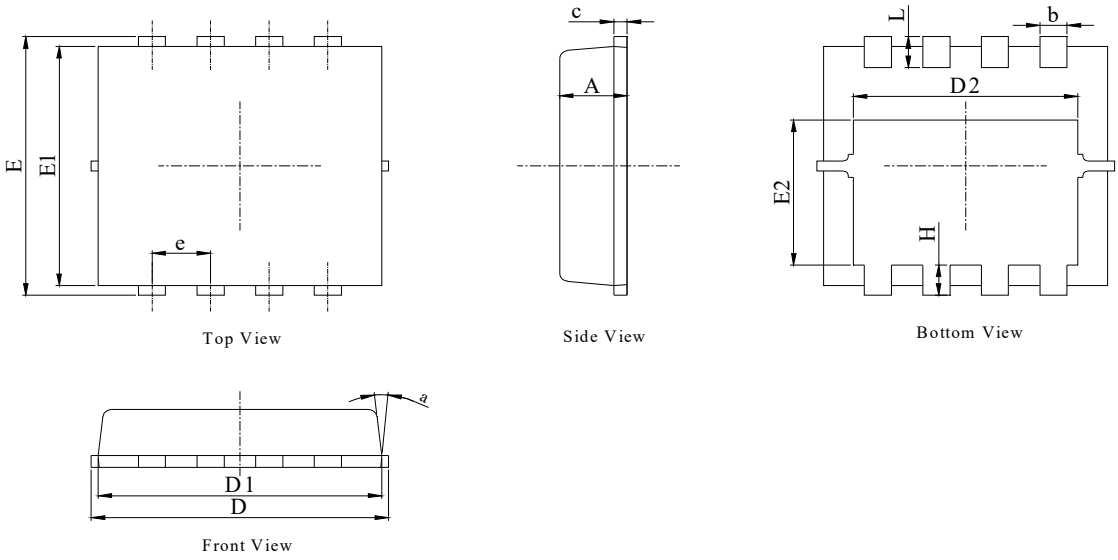


Figure 11: Normalized Maximum Transient Thermal Impedance

PDFN3x3-8L Package Information

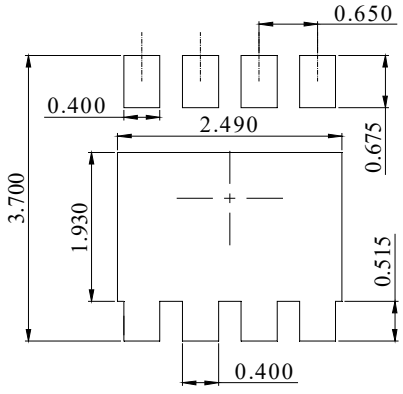
Package Outline



NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
2. ALL DIMNESIONS IN MILLIMETER (ANNGLE IN DEGREE).
3. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS.

DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.20	0.25
D	3.00	3.15	3.25
D1	2.95	3.05	3.15
D2	2.39	2.49	2.59
E	3.20	3.30	3.40
E1	2.95	3.05	3.15
E2	1.70	1.80	1.90
e		0.65 BSC	
H	0.30	0.40	0.50
L	0.25	0.40	0.50
a	---	---	15°

Recommended Soldering Footprint



DIMENSIONS:MILLIMETERS