



650V SuperJunction Power MOSFET

Features

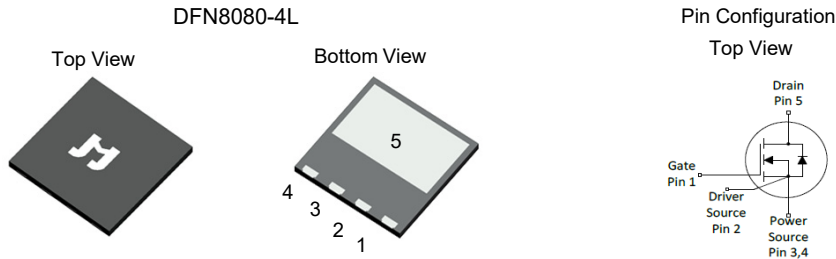
- Extremely Low Gate Charge
- Excellent Output Capacitance (C_{oss}) Profile
- Fast Switching Capability
- 100% UIS Tested, 100% R_g Tested
- Pb-free Lead Plating
- Halogen-free and RoHS-compliant

Product Summary

Parameter	Value	Unit
V_{DS}	650	V
$V_{GS(th_Typ)}$	3.5	V
I_D (@ $V_{GS} = 10V$) ⁽¹⁾	17.4	A
$R_{DS(ON_Typ)}$ (@ $V_{GS} = 10V$)	169	m Ω
$E_{oss@400V}$	5.2	μJ

Applications

- Telecom / Server Power Supplies
- Industrial Power Supplies
- UPS / Solar
- Lighting / Charger / Adapter

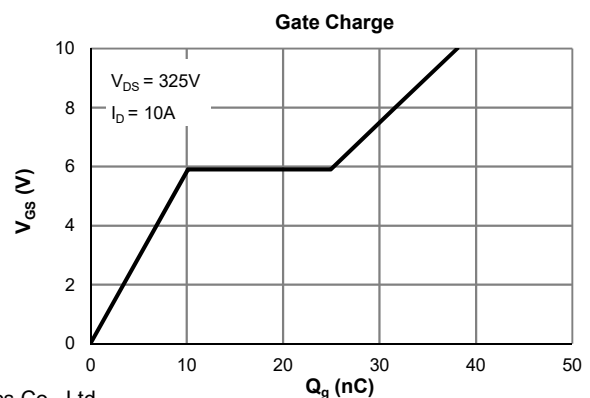
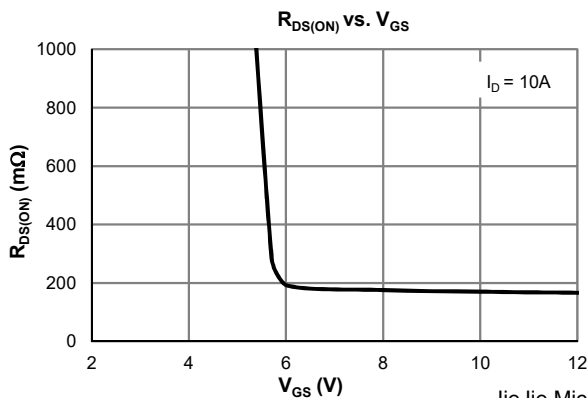


Ordering Information

Device	Package	# of Pins	Marking	MSL	T_J (°C)	Media	Quantity (pcs)
JMH65R190APLN-13	DFN8080-4L	4	H65R190A	1	-55 to 150	13-inch Reel	3000

Absolute Maximum Ratings (@ $T_A = 25^\circ C$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DS}	650	V
Gate-to-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current ⁽¹⁾	I_D	$T_C = 25^\circ C$	17.4
		$T_C = 100^\circ C$	11.0
Pulsed Drain Current ⁽²⁾	I_{DM}	75	A
Avalanche Current ⁽³⁾	I_{AS}	9.0	A
Avalanche Energy ⁽³⁾	E_{AS}	405	mJ
Power Dissipation ⁽⁴⁾	P_D	$T_C = 25^\circ C$	156
		$T_C = 100^\circ C$	63
Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C



**Electrical Characteristics** (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	650			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 650\text{V}, V_{GS} = 0\text{V}$			1.0	μA
Gate-Body Leakage Current	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 30\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2.5	3.5	4.5	V
Static Drain-Source ON-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}, I_D = 10\text{A}$		169	190	m Ω
Diode Forward Voltage	V_{SD}	$I_S = 1\text{A}, V_{GS} = 0\text{V}$		0.75	1.0	V
Diode Continuous Current	I_S	$T_C = 25^\circ\text{C}$			156	A
DYNAMIC PARAMETERS ⁽⁵⁾						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}, V_{DS} = 100\text{V}, f = 1\text{MHz}$		1560		pF
Output Capacitance	C_{oss}			61		pF
Reverse Transfer Capacitance	C_{rss}			11.7		pF
Effective output capacitance, energy related	$C_{o(er)}$	$V_{GS} = 0\text{V}, V_{DS} = 0 \text{ to } 400\text{V}$		65		pF
Effective output capacitance, time related	$C_{o(tr)}$			274		pF
Gate Resistance	R_g	$V_{GS} = 0\text{V}, V_{DS} = 0\text{V}, f = 1\text{MHz}$		8.9		Ω
SWITCHING PARAMETERS ⁽⁵⁾						
Total Gate Charge (@ $V_{GS} = 10\text{V}$)	Q_g	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 325\text{V}, I_D = 10\text{A}$		38		nC
Gate Source Charge	Q_{gs}			10.1		nC
Gate Drain Charge	Q_{gd}			14.8		nC
Turn-On DelayTime	$t_{D(on)}$	$V_{GS} = 10\text{V}, V_{DS} = 325\text{V}$ $R_L = 3.25\Omega, R_{GEN} = 6\Omega$		17.8		ns
Turn-On Rise Time	t_r			22		ns
Turn-Off DelayTime	$t_{D(off)}$			257		ns
Turn-Off Fall Time	t_f			20		ns
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 10\text{A}, di/dt = 100\text{A}/\mu\text{s}$		340		ns
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F = 10\text{A}, di/dt = 100\text{A}/\mu\text{s}$		4.4		μC
Peak Diode Recovery Voltage Slope	dv/dt	$I_F \leq 10\text{A}, di/dt = 200\text{A}/\mu\text{s}, V_{DS} = 400\text{V}$		15		V/ns
MOSFET dv/dt Ruggedness	dv/dt	$V_{DS} = 0 \dots 400\text{V}$		50		V/ns

Thermal Performance

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	42	50	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.80	1.0	$^\circ\text{C}/\text{W}$

Notes:

1. Computed continuous current assumes the condition of T_{J_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under $T_{J_Max} = 150^\circ\text{C}$.
3. This single-pulse measurement was taken under the following condition [$L = 10\text{mH}, V_{GS} = 10\text{V}, V_{DS} = 50\text{V}$] while its value is limited by $T_{J_Max} = 150^\circ\text{C}$.
4. The power dissipation P_D is based on $T_{J_Max} = 150^\circ\text{C}$.
5. This value is guaranteed by design hence it is not included in the production test.



Typical Electrical & Thermal Characteristics

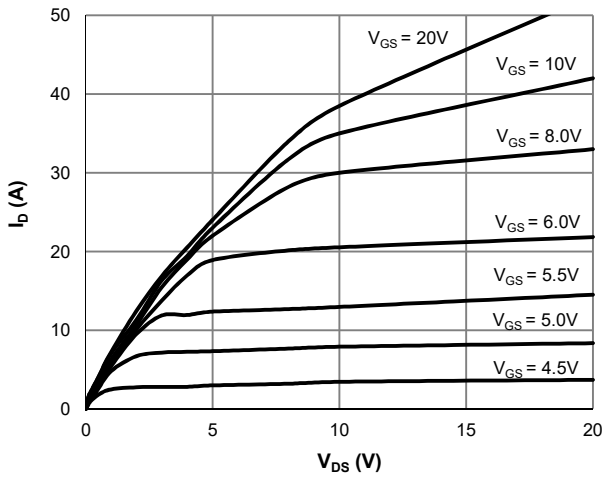


Figure 1: Saturation Characteristics

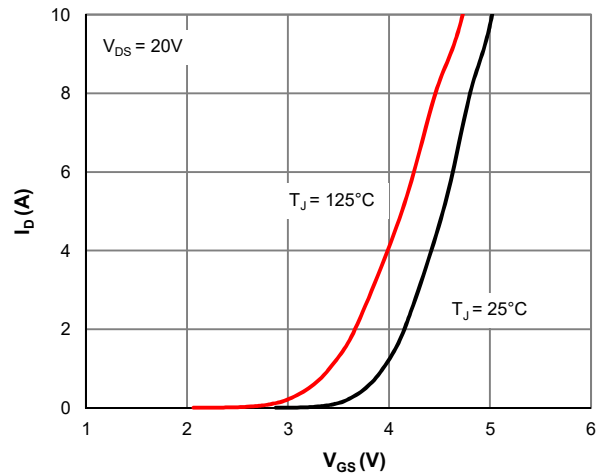


Figure 2: Transfer Characteristics

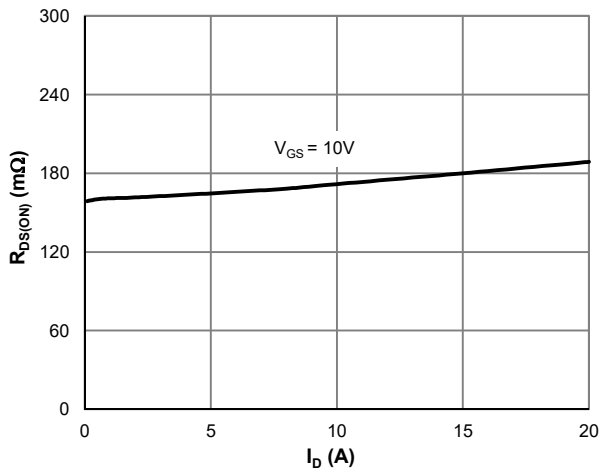


Figure 3: $R_{DS(ON)}$ vs. Drain Current

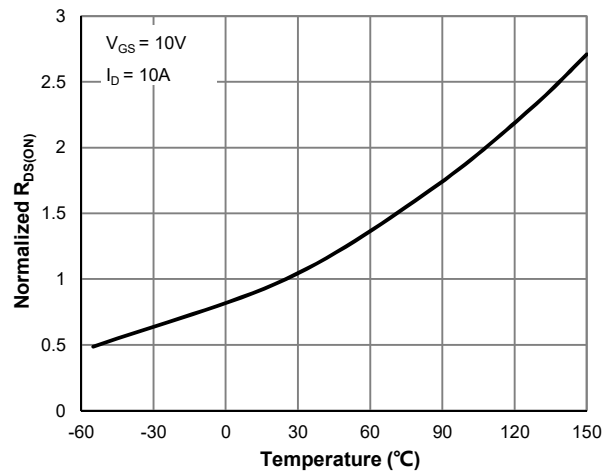


Figure 4: $R_{DS(ON)}$ vs. Junction Temperature

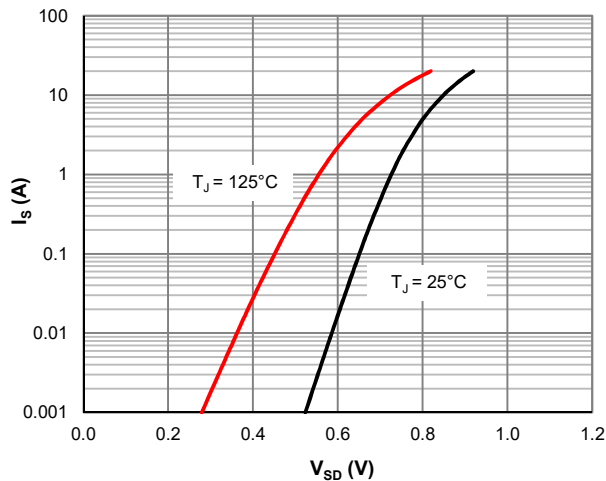


Figure 5: Body-Diode Characteristics

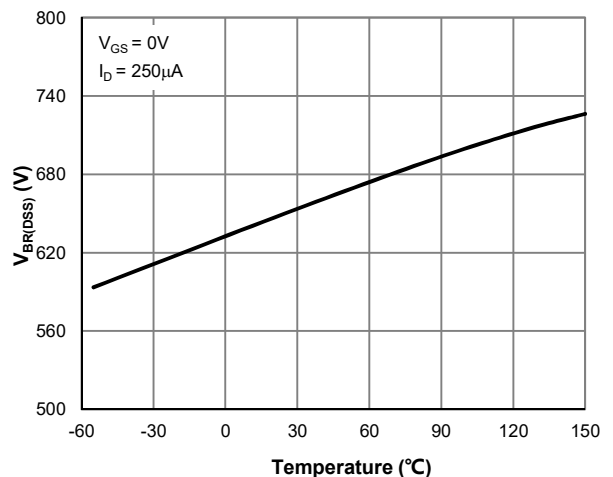
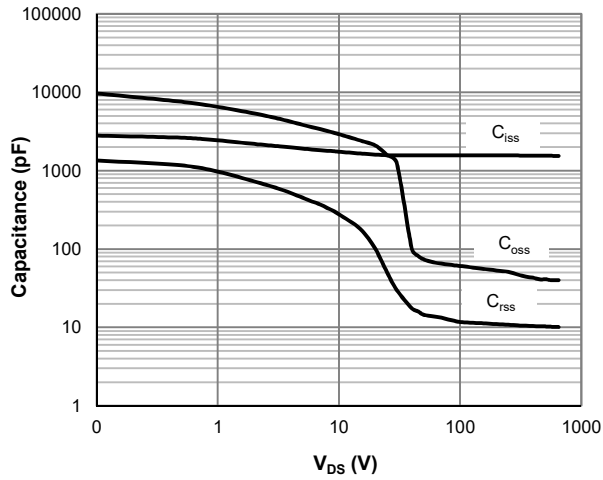
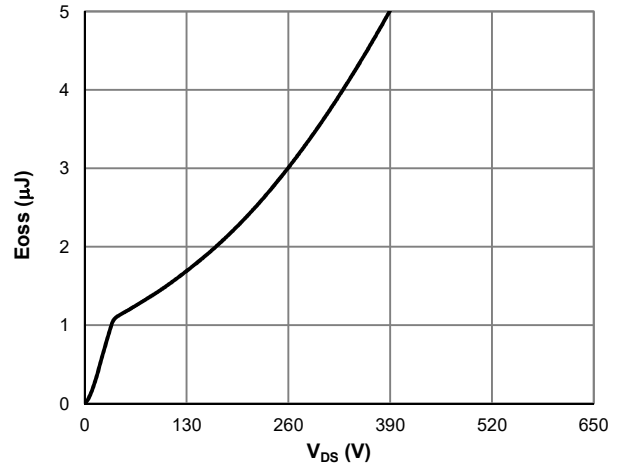
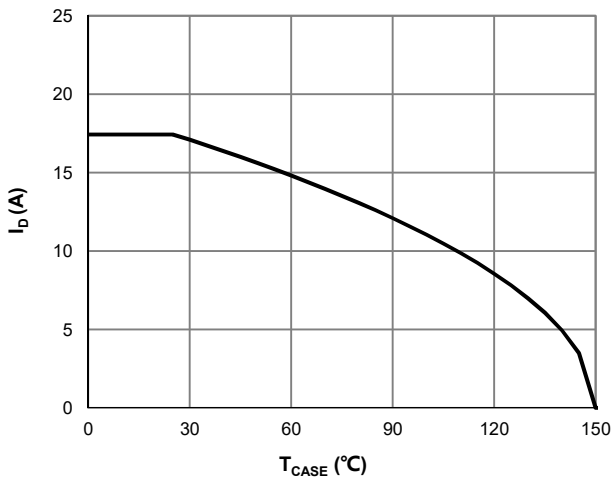
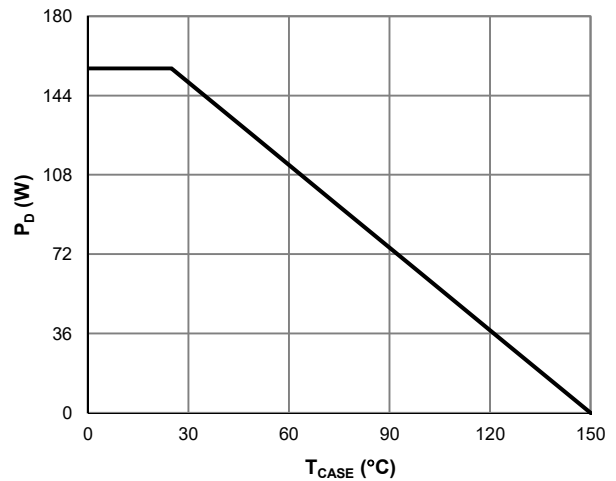
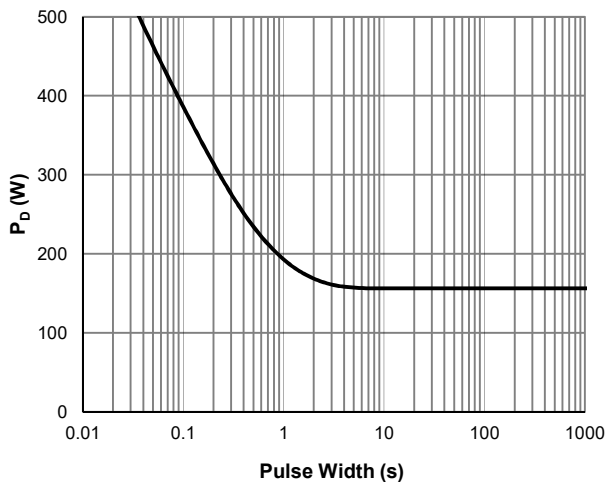
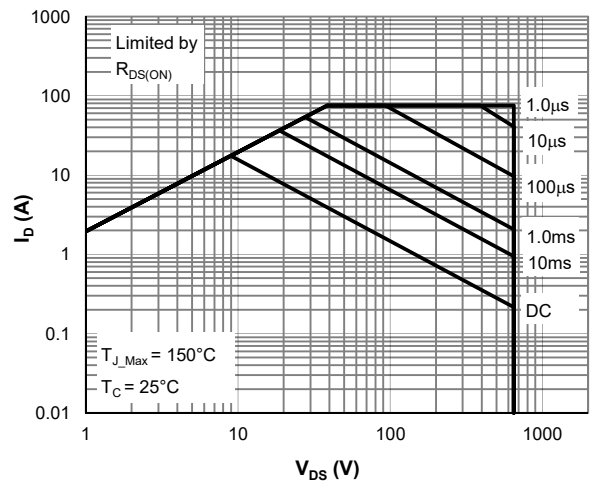


Figure 6: $V_{BR(DSS)}$ vs. Junction Temperature

Typical Electrical & Thermal Characteristics

Figure 7: Capacitance Characteristics

Figure 8: Coss Stored Energy

Figure 9: Current De-rating

Figure 10: Power De-rating

Figure 11: Single Pulse Power Rating, Junction-to-Case

Figure 12: Maximum Safe Operating Area



Typical Electrical & Thermal Characteristics

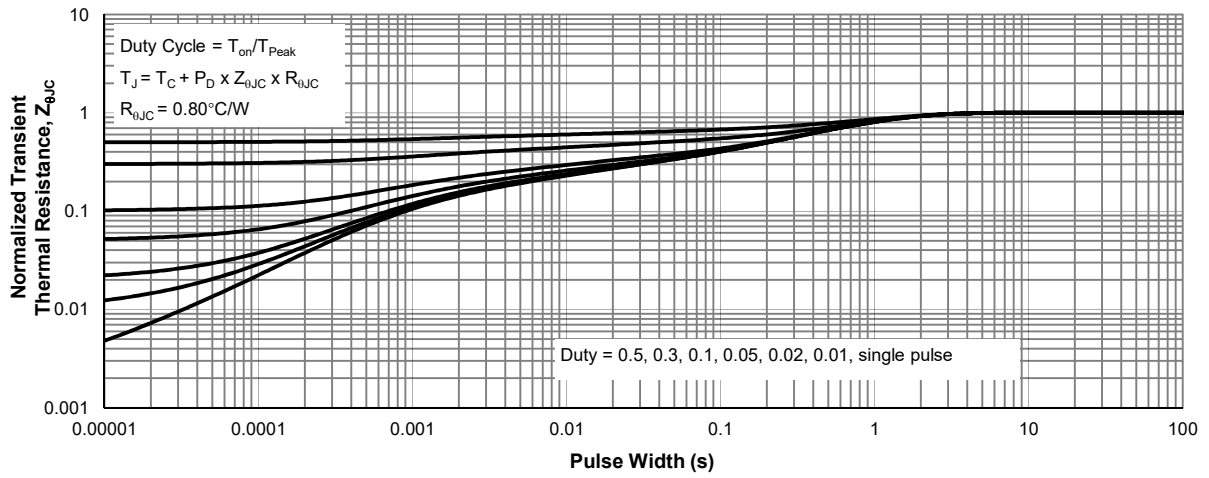
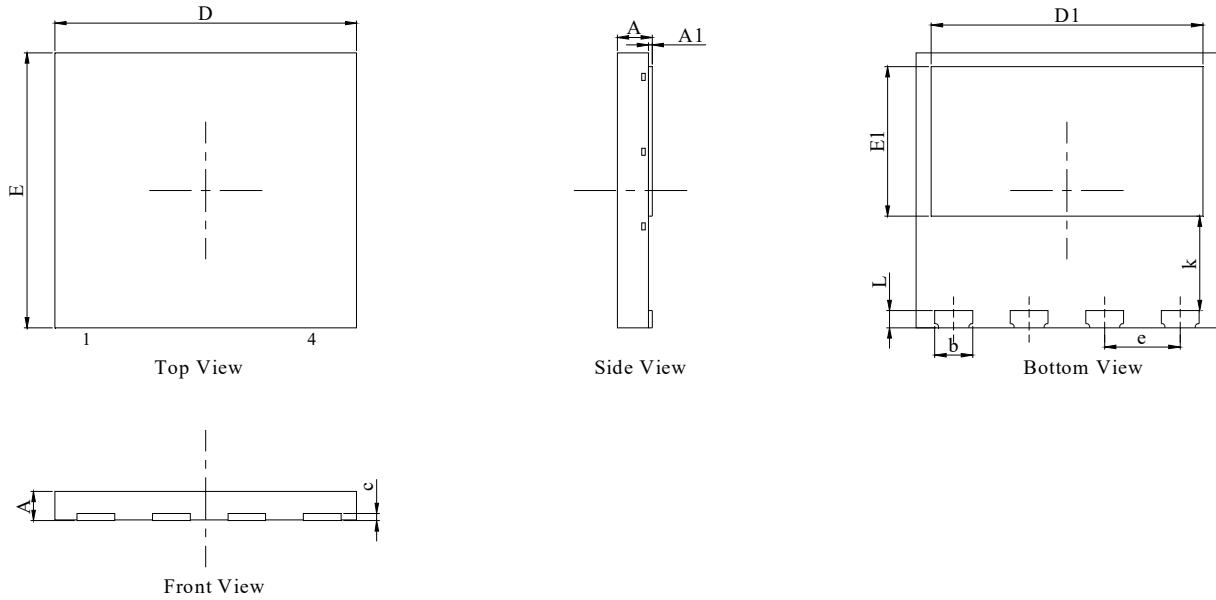


Figure 13: Normalized Maximum Transient Thermal Impedance

DFN8080-4L Package Information
Package Outlines


DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.85	0.90	0.95
A1	--	--	0.05
b	0.95	1.00	1.05
c	--	0.20	--
D	7.90	8.00	8.10
D1	7.10	7.20	7.30
E	7.90	8.00	8.10
E1	4.25	4.35	4.45
L	0.40	0.50	0.60
k	2.75		
e	2.00 BSC		

Recommended Soldering Footprint
